

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (canceled)

2. (canceled)

3. (original) A microprocessor for processing various assembler codes, comprising:

a multiplexer having a first input, a second input for receiving a 0 value, and a third input receiving a parameter designating a respective assembler code and, depending on how the parameter is set, a different relative addressing takes place;

a program counter;

a computation unit for computing relative addresses;

an adding unit connected between said program counter and said computation unit, said adding unit having a first input connected to said program counter, a second input connected to

said multiplexer, and an output connected to said computation unit; and

a memory for storing an instruction length and having an output connected to said first input of said multiplexer.

4. (original) A microprocessor for processing various assembler codes, comprising:

a multiplexer having a first input, a second input for receiving a 0 value, and a third input receiving a parameter designating a respective assembler code and, depending on how the parameter is set, a different relative addressing takes place;

a program counter;

a computation unit for computing relative addresses;

an subtracting unit connected between said program counter and said computation unit for the relative addresses, said subtracting unit having a first input connected to said program counter, a second input connected to said multiplexer, and an output connected to said computation unit; and

a memory for storing an instruction length and having an
output connected to said first input of said multiplexer.

5 - 7. (canceled)